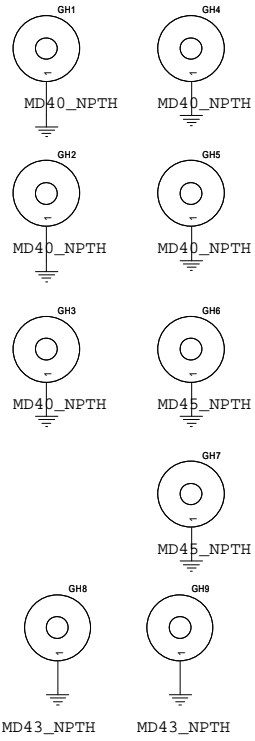
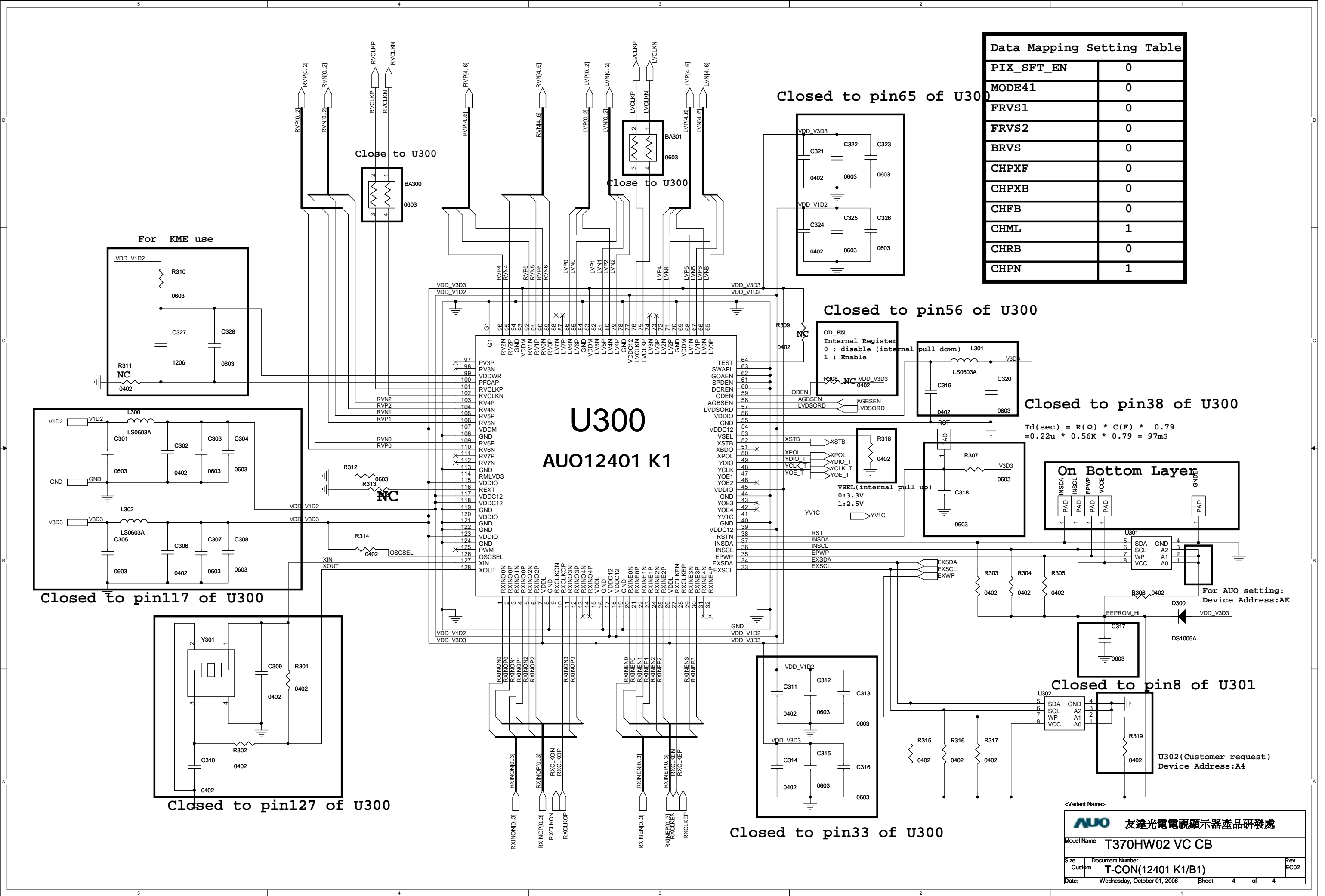


Normal / Mirror Setting Table		
	Normal	Mirror
YDIOU	O	X
YDIOD	X	O
U_DF	H	L
U_DB	L	H

LVDSORD Setting Table		
LORD_INV	LVDSORD	MODE
0	0	NS
0	1	JEIDA
1	1	NS
1	0	JEIDA

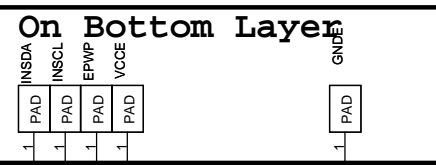




Data Mapping Setting Table	
PIX_SFT_EN	0
MODE41	0
FRVS1	0
FRVS2	0
BRVS	0
CHPXF	0
CHPXB	0
CHFB	0
CHML	1
CHRB	0
CHPN	1

Closed to pin38 of U300

$$T_d(\text{sec}) = R(\Omega) * C(F) * 0.79$$
$$= 0.22\mu * 0.56K * 0.79 = 97\text{ms}$$



For AUO setting:
Device Address:AE

DS1005A

Closed to pin8 of U301

U302(Customer request)
Device Address:A4